

**METHOD FOR ALLOWING DISTRIBUTED HIGH PERFORMANCE  
COHERENT MEMORY WITH FULL ERROR CONTAINMENT**

**ABSTRACT OF THE DISCLOSURE**

The present invention provides for a method and system for ensuring error containment in packet based systems which are capable of large scalability. In operation, an error bit travels with each data packet and if the bit is set any device which receives the data packet acts to contain that packet. Thus, the error message travels only as far as the error data and does not stop processing at locations not affected by the error. Any system resource, upon receipt of a set  
5 error bit, must act to correct the fault.